

DIGITAL  
SEQUENCE:

1

0

1

1

0

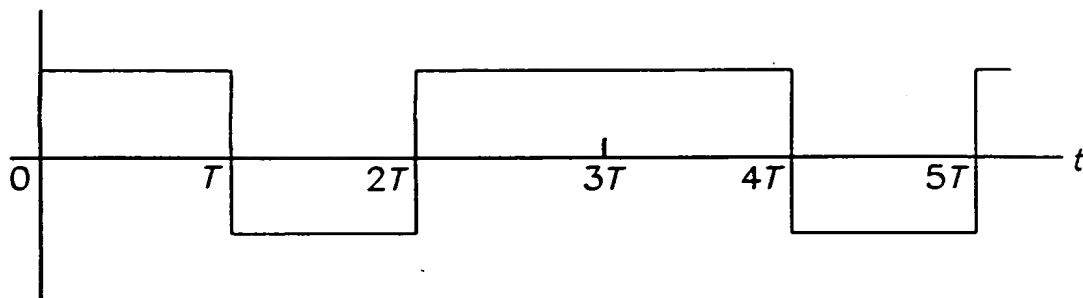


FIG. 1A

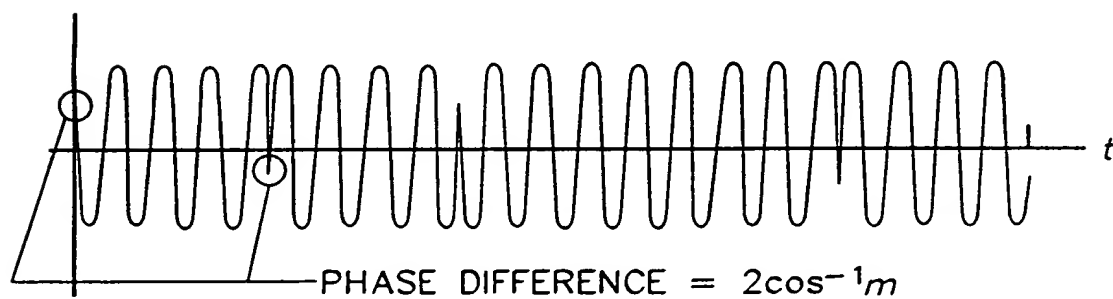


FIG. 1B

363600 363600

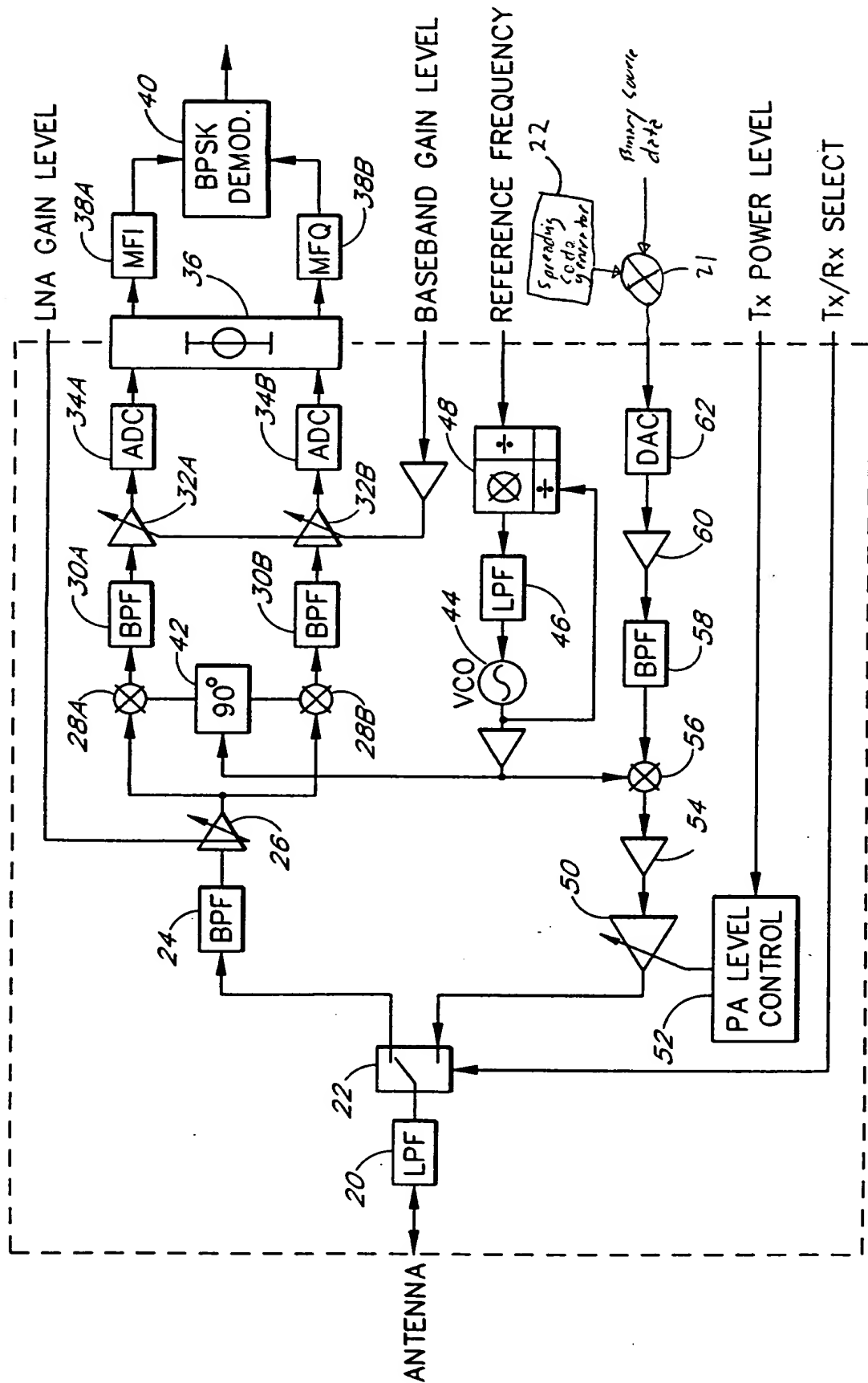


FIG. 2

360250" 35T4E60

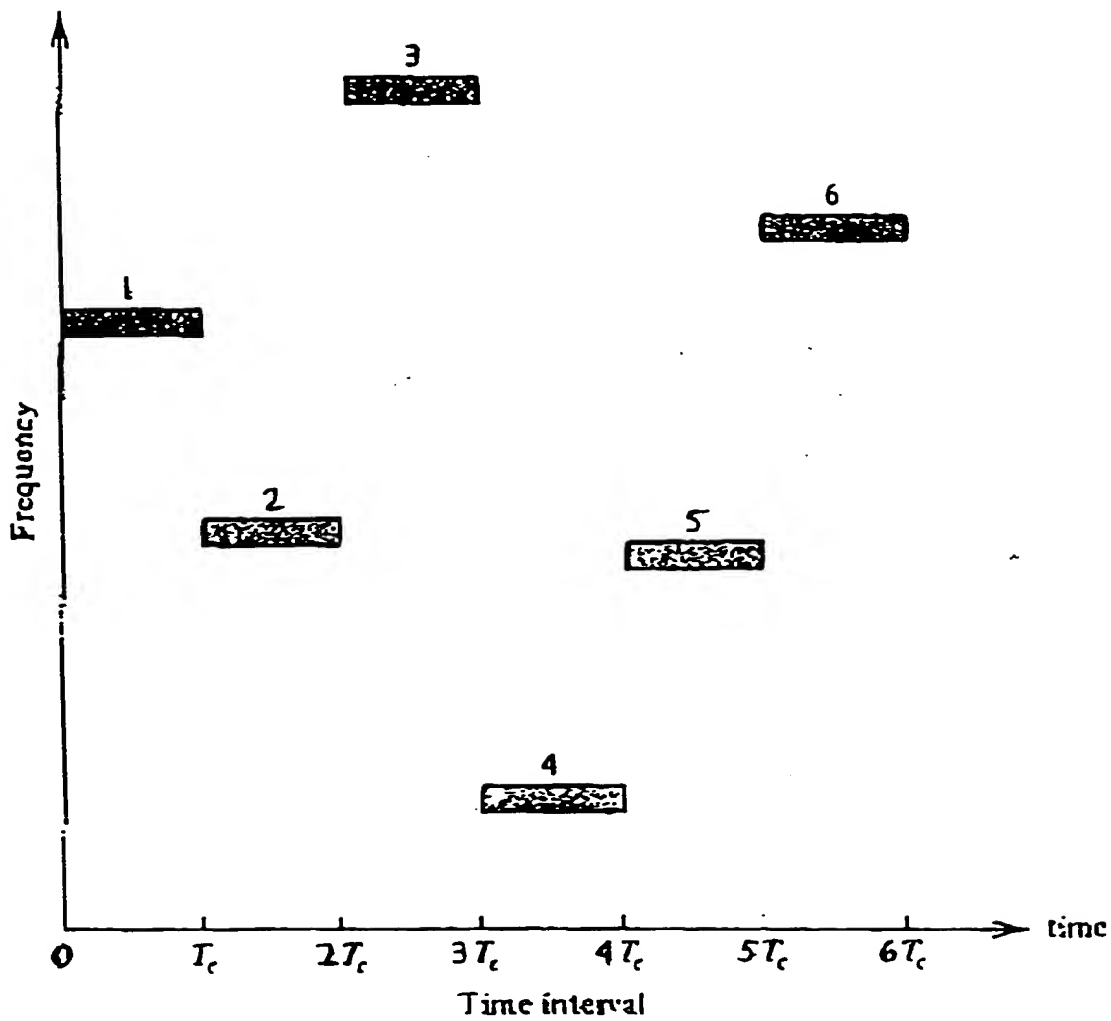


Figure 3

Timing diagram for a Frequency Hopping (FH) system. The diagram shows a sequence of blocks: 1 ms, Tx, Rx, Tx, Rx, 1 ms, Tx, Rx, Tx, Rx, followed by an ellipsis. A bracket under the first four blocks (Tx, Rx, Tx, Rx) is labeled "Center Frequency A". A bracket under the next four blocks (Tx, Rx, Tx, Rx) is labeled "Center Frequency B". Vertical lines mark the start of the first and second 1 ms blocks, and the end of the second 1 ms block. The time interval between the first and second 1 ms blocks is labeled  $T_C$ . The time interval between the end of the second 1 ms block and the end of the third 1 ms block is labeled  $2T_C$ . A label "Synthesiser switching delay" points to the 1 ms block.

FIGURE 5

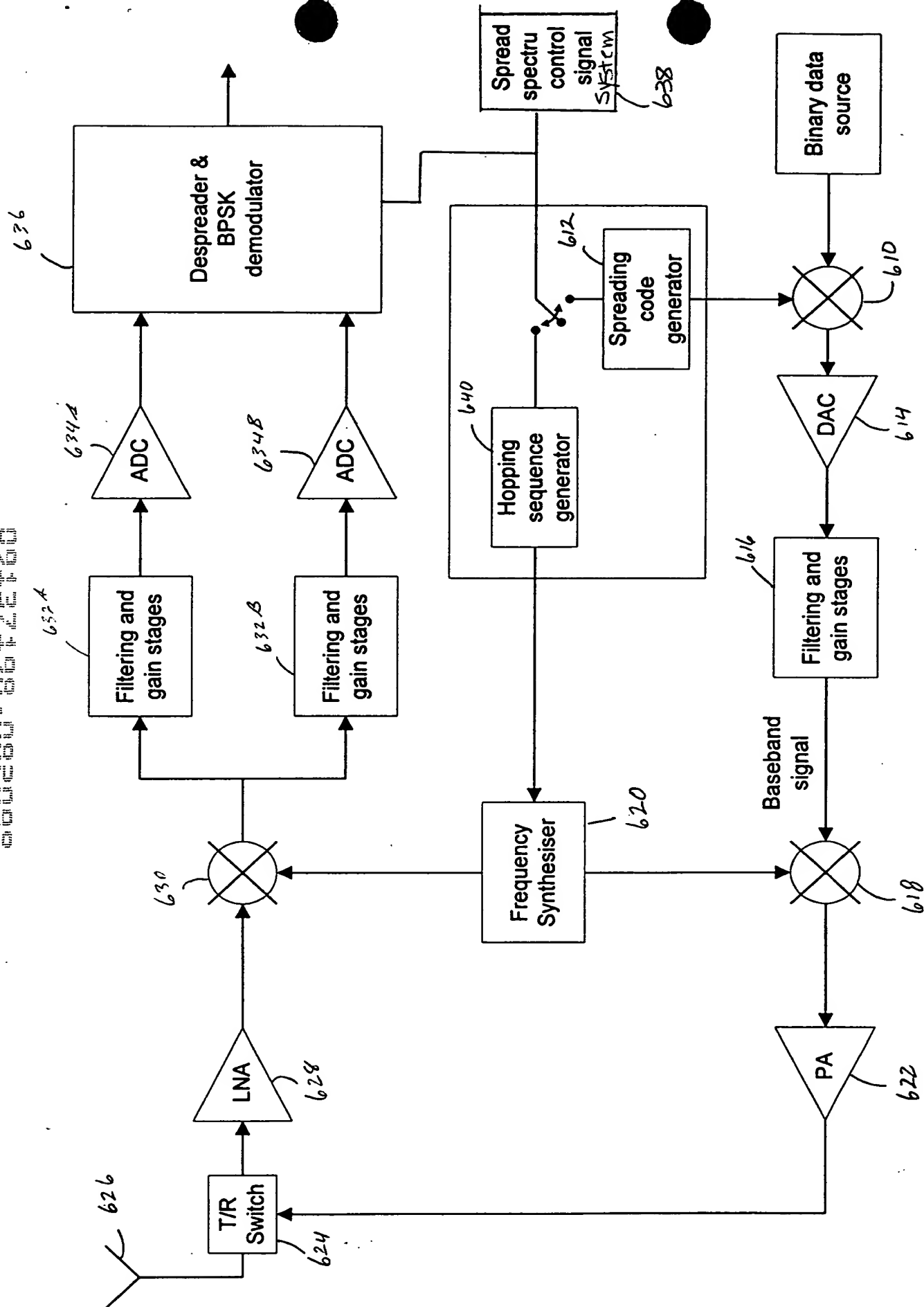


Figure 6

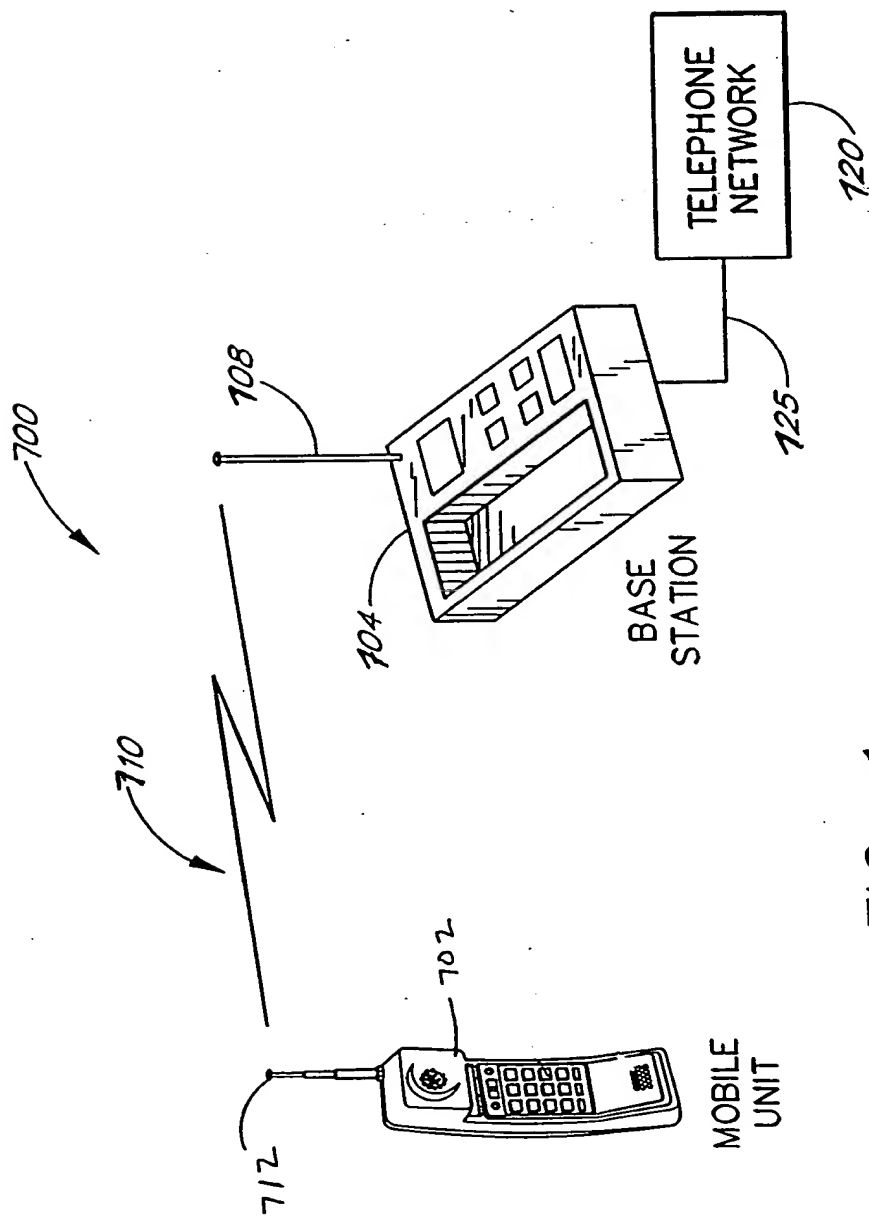


FIG. 7